

Sheet: S8000\_WDC\_neu\_01

Sheet: S8000\_WDC\_01

Sheet: S8000\_WDC\_02

Sheet: S8000\_WDC\_03

Sheet: S8000\_WDC\_04

Sheet: S8000\_WDC\_05

/IORD<  
  
 /RD<  
  
 /IOREQ<  
  
  
 /EXTBAI<  
 /EXTBAO<  
 BUS\_MEM<  
 BUS\_DATA<  
 BUS\_ADDR<  
 BUS\_PIO<  
 DMA\_BAO<  
  
 /LR/W<  
 /LD/S<  
 /OEa<  
 OEa<  
 LEXTAD<  
 BUS\_2PRAM\_YA<  
 U5\_6<  
 U5\_3<  
 U30\_11<  
  
 /RST<  
 /LAS<  
 /EXTBUSREQ<  
 /EXTPWRBAD<  
 Z80\_OE\_A\_2PRAM<  
  
  
 U79-00<

File: S8000\_WDC\_neu\_01.sch

/LATCH\_SELD  
 /SRAM\_SEL\_2D  
 /SRAM\_SEL\_1D  
 /NMID  
 /IOWRD  
 /IORDD  
 /BUSACKD  
 /BUSREQD  
 M1D  
 /MREQD  
 /RDD  
 /WRD  
 /IOREQD  
 IOREQD  
 /RFSHD

BUS\_DATA<  
 BUS\_ADDR<

File: S8000\_WDC\_01.sch

>/SRAM\_SEL\_2  
 >/SRAM\_SEL\_1  
 >/NMI  
  
  
  
  
 >/RD  
 >/WR  
  
  
  
 >BUS\_MEM  
 >BUS\_DATA  
 >BUS\_ADDR  
  
 >DMA\_BAO  
 >/MEM\_WR  
 >SYS\_BCLK

File: S8000\_WDC\_02.sch

>/BUSACK  
 </BUSREQ  
  
 </MREQ  
 </RD  
 </WR  
 </IOREQ

>/EXTBAI  
 >/EXTBAO  
  
 <BUS\_DATA  
 <BUS\_ADDR  
 <BUS\_PIO  
 <DMA\_BAO  
  
 >SYS\_BCLK

File: S8000\_WDC\_03.sch

>/IORD

>/LR/W  
 >/LD/S

</OEa  
 <OEa

>LEXTAD  
 >BUS\_2PRAM\_YA  
 >U5\_6  
 >U5\_3  
 >U30\_11  
 >U79-00  
 >/RST  
 >/LAS

>Z80\_OE\_A\_2PRAM

File: S8000\_WDC\_04.sch

>BUS\_PIO

File: S8000\_WDC\_05.sch

File: S8000_WDC_neu.sch			
Sheet: /			
<b>Title:</b>			
Size: A4	Date: 2 dec 2015		
RTCad E.D.A. eeschema (2012-01-19 BZR 3256)-stable		<b>Rev:</b>	
		Id: 1/7	











